

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) In a system for decoding variable length prefix codes in a bit stream, a method comprising:

reading, from the bit stream, a number of bits sufficient to store a longest variable length code of the system;

selecting a predetermined number of bits from the bits read; and

obtaining, from a data structure, in accordance with an actual value of the bits selected, at least a decoded value and a validity indicator associated with a variable length code; and

applying a prefix oriented decoding method to the bits initially read from the bit stream when the decoded value is indicated to be invalid.

2. (Cancelled)

3. (Original) The method of claim 1, wherein reading the number of bits comprises making the specified number of bits accessible for future operations.

4. (Original) The method of claim 1, wherein selecting the number of bits comprises making the specified number of bits accessible for future operations, faster than reading the same number of bits.

5. (Original) The method of claim 1, wherein the predetermined number of bits comprises the maximal number of bits to be used as an index to the data structure.

6. (Original) The method of claim 1, wherein the validity indicator indicates whether the decoded value is valid.

7. (Original) The method of claim 1, wherein the data structure used to obtain at least the decoded value and validity indicator associated with a variable length code comprises a memory area containing at least the decoded value and validity indicator for each bit combination that can be formed from the predetermined number of bits.

8. (Currently amended) The method of claim 21, wherein the prefix oriented decoding method further comprises a method of variable length decoding that employs variable length code prefix properties during decoding.

9. (Currently amended) An article comprising: a machine accessible medium having a plurality of machine readable instructions, wherein when the instructions are executed by a processor, the instructions provide for decoding of variable length prefix codes in a bit stream by

reading, from the bit stream, a number of bits sufficient to store a longest variable length code of the system;

selecting a predetermined number of bits from the bits read; and

obtaining, from a data structure, in accordance with an actual value of the bits selected, at least a decoded value and validity indicator associated with a variable length code; and

applying a prefix oriented decoding method to the bits initially read from the bit stream when the decoded value is indicated to be invalid.

10. (Cancelled)

11. (Original) The article of claim 9, wherein instructions for reading the number of bits comprise instructions for making the specified number of bits accessible for future operations.

12. (Original) The article of claim 9, wherein instructions for selecting the number of bits comprise instructions for making the specified number of bits accessible for future operations, faster than reading the same number of bits.

13. (Original) The article of claim 9, wherein the predetermined number of bits comprises the maximal number of bits to be used as an index to the data structure.

14. (Original) The article of claim 9, wherein the validity indicator indicates whether the decoded value is valid.

15. (Original) The article of claim 9, wherein the data structure used to obtain at least the decoded value and validity indicator associated with a variable length code comprises a memory area containing at least the decoded value and validity indicator for each bit combination that can be formed from the predetermined number of bits.

16. (Currently amended) The article of claim 109, wherein prefix oriented decoding method further comprises a method of variable length decoding that employs variable length code prefix properties during decoding.

17. (Currently amended) A system for decoding variable length prefix codes in a bit stream, comprising:

logic to read from the bit stream a number of bits sufficient to store a longest variable length code of the system;

logic to select a predetermined number of bits from the bits read; and

logic to obtain from a data structure, in accordance with the actual value of the bits selected, at least a decoded value and a validity indicator associated with a variable length code; and

logic to apply a prefix oriented decoding method to the bits initially read from the bit stream when the decoded value is indicated to be invalid.

18. (Cancelled)

19. (Original) The system of claim 17, wherein logic to read the number of bits comprises logic to make the specified number of bits accessible for future operations.

20. (Original) The system of claim 17, wherein logic to select the number of bits comprises logic to make the specified number of bits accessible for future operations, faster than logic to read the same number of bits.

21. (Original) The system of claim 17, wherein the predetermined number of bits comprises the maximal number of bits to be used as an index to the data structure.

22. (Original) The system of claim 17, wherein the validity indicator indicates whether the decoded value is valid.

23. (Original) The system of claim 17, wherein the data structure used to obtain at least the decoded value and validity indicator associated with a variable length code comprises a memory area containing at least the decoded value and validity indicator for each bit combination that can be formed from the predetermined number of bits.

24. (Currently amended) The system of claim ~~17~~17, wherein prefix oriented decoding method further comprises a method of variable length decoding that employs variable length code prefix properties during decoding.